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Notice of Allowability	Application No.	Applicant(s)	
	09/524,901	QI ET AL.	
	Examiner Daniel D. Chang	Art Unit 2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to Amendment filed June 1, 2004.
2. The allowed claim(s) is/are 1-7 and 9-22.
3. The drawings filed on _____ are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

Daniel D. Chang
Primary Examiner
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Acknowledgement

Receipt is acknowledged of the Amendment filed June 1, 2004.

Drawings

The drawings are objected to as failing to comply with 37 CFR 1.84(i) & 37 CFR 1.84(p) because lines, numbers & letters are not uniformly thick and well defined, clean, durable, and black (poor line quality). Also, some characters are too small (see figures 3 and 4).

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Eustace Isidore on July 7, 2004.

The application has been amended as follows:

Claim 13, lines 10 and 13, "first transistor" has been deleted and replaced by --first-type transistor--.

Claim 13, lines 11, 12, 14, and 15, "second transistor" has been deleted and replaced by --second-type transistor--.

On page 6 of the amendment filed June 1, 2004, the second occurring claim 13 which states “13. (canceled)” has been deleted.

Claim 17, line 2, “first transistor” has been deleted and replaced by --first-type transistor--.

Claim 17, line 6, “second transistor” has been deleted and replaced by --second-type transistor--.

Claim 19, lines 2-3, “said plurality of transistors each have a control node and a first and second data node and wherein” has been deleted.

Claim 19, line 3, “said” after “controls” has been deleted and replaced by --a--.

Claim 20, line 1, “12” has been deleted and replaced by --13--.

Claim 21, lines 2-3, “said plurality of transistors each have a control node and a first and second data node and wherein” has been deleted.

Claim 21, line 3, “said” after “controls” has been deleted and replaced by --a--.

Claim 22, line 1, “20” has been deleted and replaced by --12--.

Reasons for Allowance

Claims 1-7 and 9-22 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Yamashita et al., taken alone or in combination of other references, does not teach or fairly suggest a high speed static multiplexer comprising, among other things, a pull-up circuit which includes a first-type transistor and a plurality of second-type transistors, each having a control node, and a first and second data node, wherein said second data node of each of said plurality of second-type transistors is coupled to the control node of said first-type transistor, said

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first data node of each of said plurality of second-type transistors is coupled to a respective one of said plurality of data inputs, and said control node of each of said plurality of second-type transistors is coupled to one of said at least one select input, as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Daniel D. Chang
Primary Examiner
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DC

**DANIEL CHANG
PRIMARY EXAMINER**